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FILING DATE

APPLICATION NO. 10/657,362

09/09/2003

PIRST NAMED INVENTOR Michael A. Killian

02 P 15176 US / INTECH

Kristin R. Hayes

October 6.

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TITLE OF INVENTION: FUSE LATCH CIRCUIT WITH NON-DISRUPTIVE RE-INTERROGATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Killian, et al.

Docket No.:

02 P 15176 US / INTECH 3.

Serial No:

10/657,362

Art Unit:

2816

Date Filed:

September 8, 2003

Title:

Fuse Latch Circuit with Non-Disruptive Re-Interrogation

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (571) 273-2885 on the date shown above:

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Respectfully submitted,

Kristin R. Hayes

Legal Assistant

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